Profectis
Technischer Kundendienst

Technische Unterlage

Best.-Nr.

QUELLE.

411.793
195.489
229.810
214.336

LCD-TV

Chassis: 17MB11

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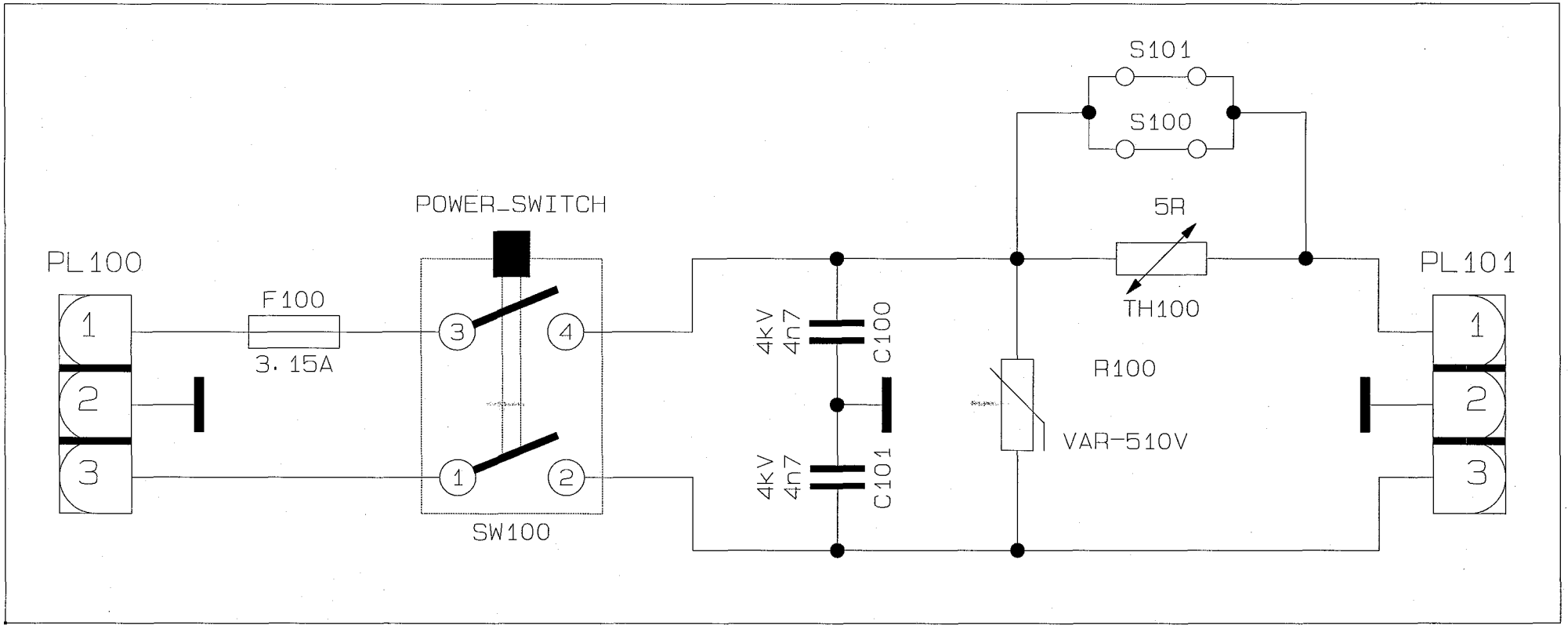
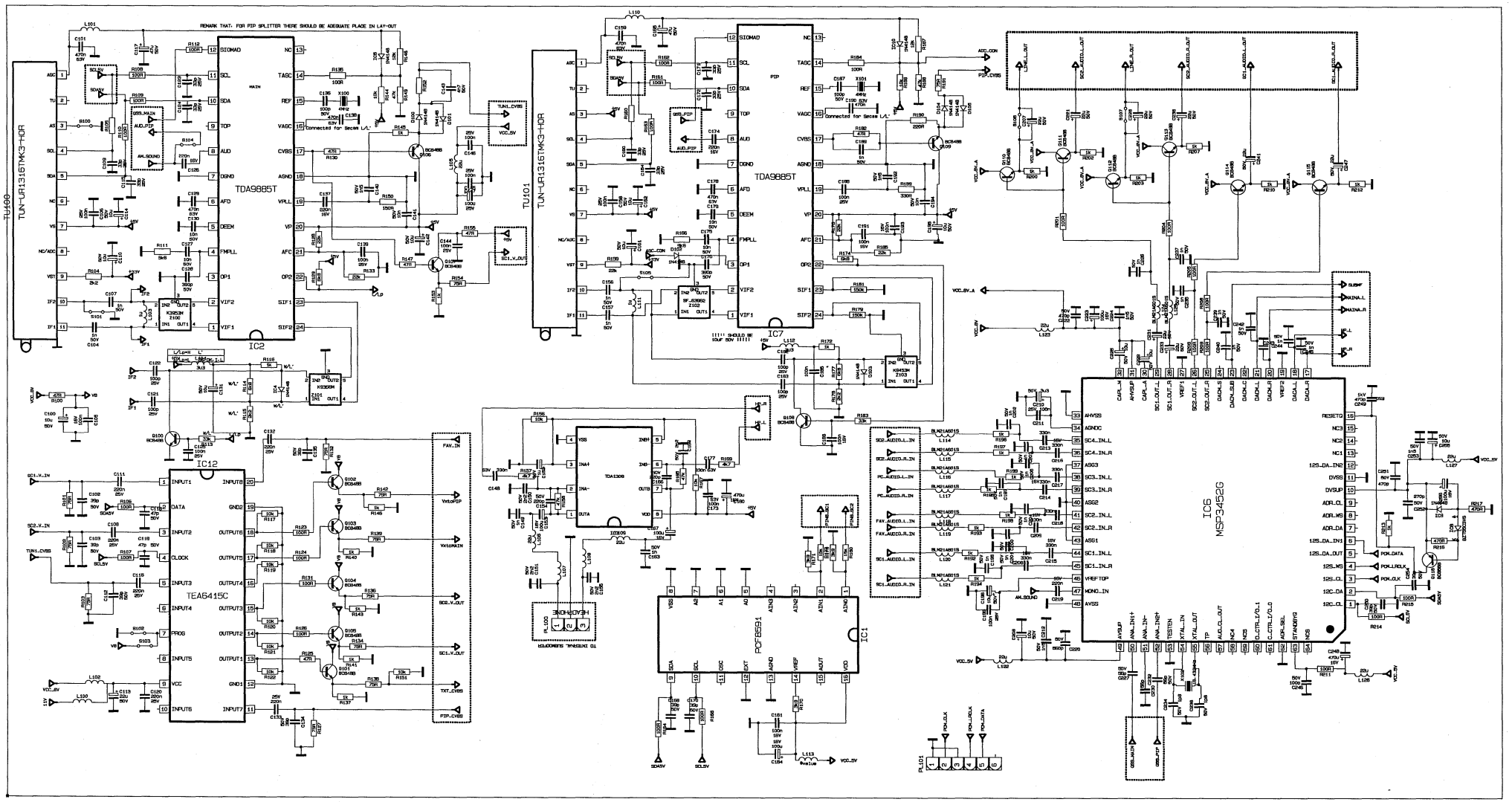
universum. FT81051/FT8154/FT8145

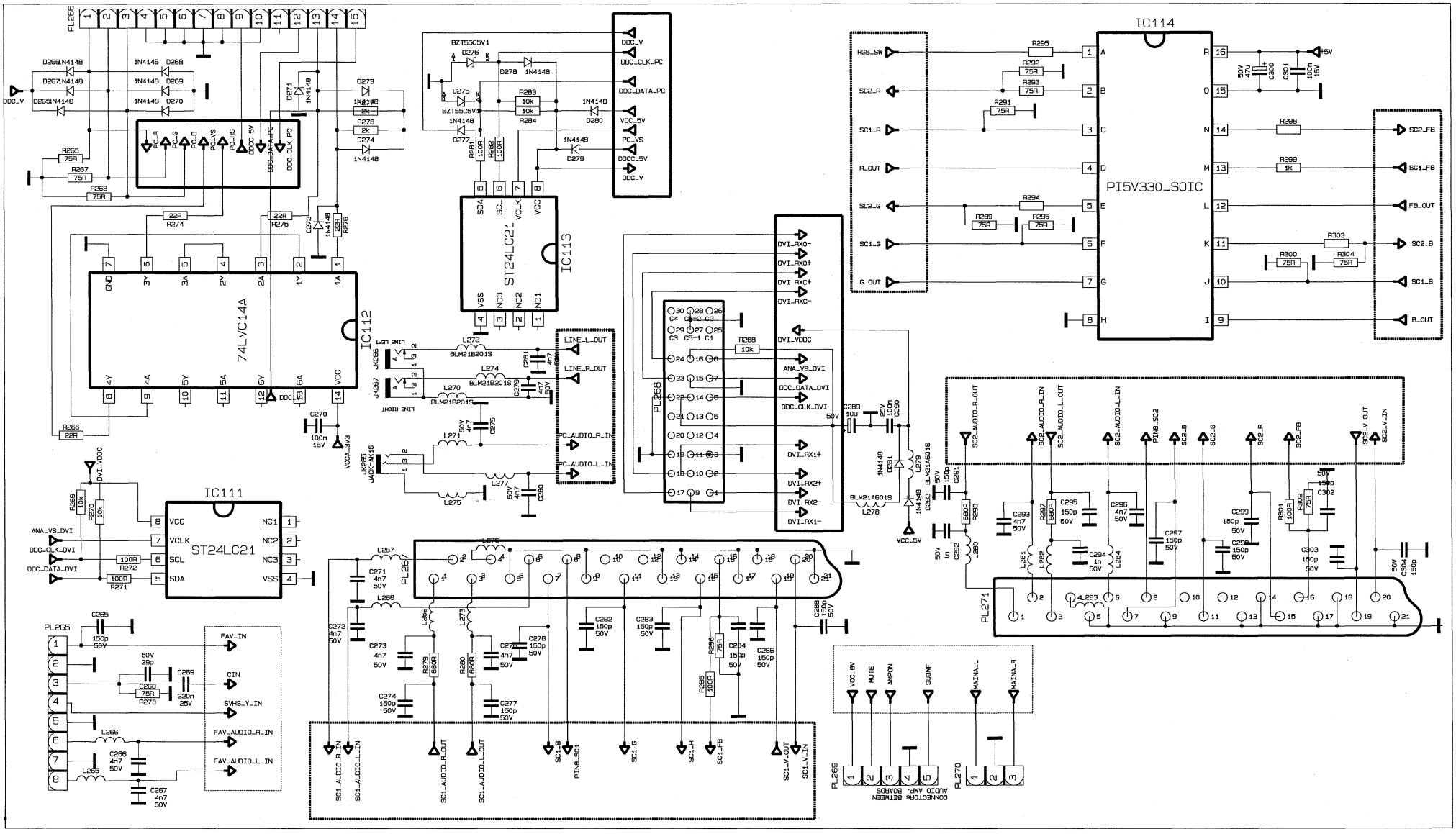
(Nur für TKD-Stellen)

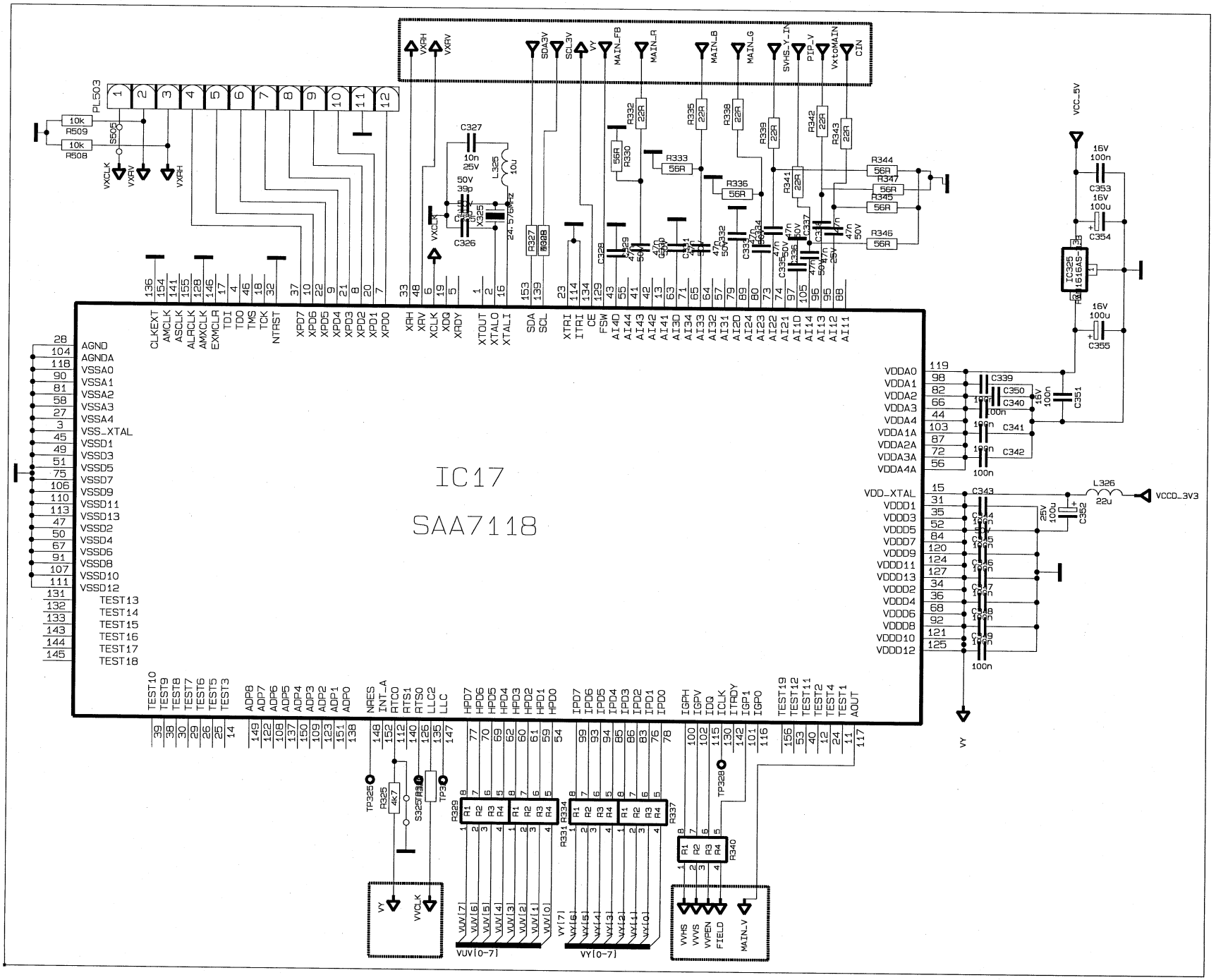
Profectis GmbH Technischer Kundendienst
Kundendienst Zentrale Duisburger Str. 57, 90451 Nürnberg
Bei allen Reparaturen sind die gültigen Sicherheitsvorschriften zu beachten!

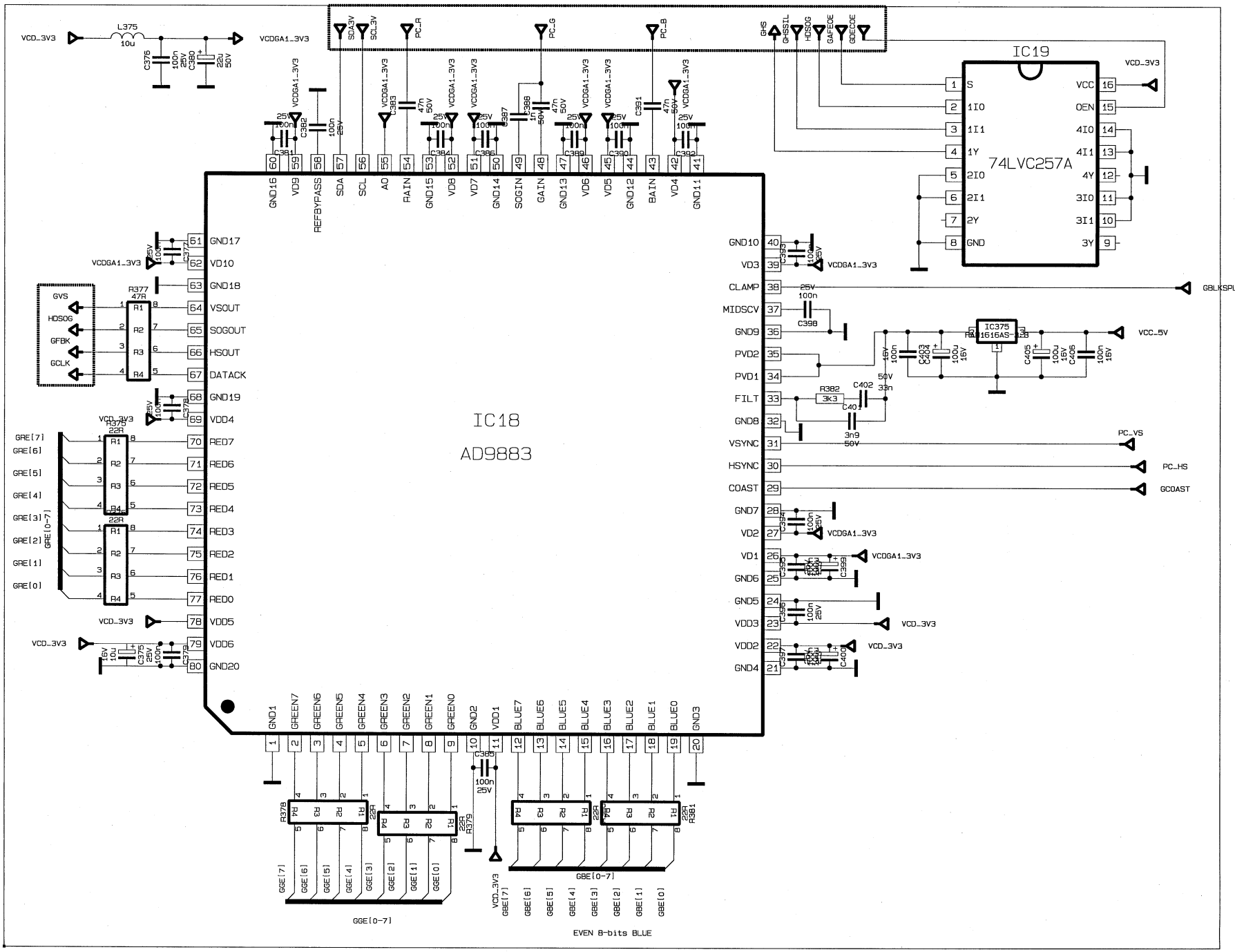
Nachdruck nicht gestattet
Änderung vorbehalten
Technischer Support 10/2005

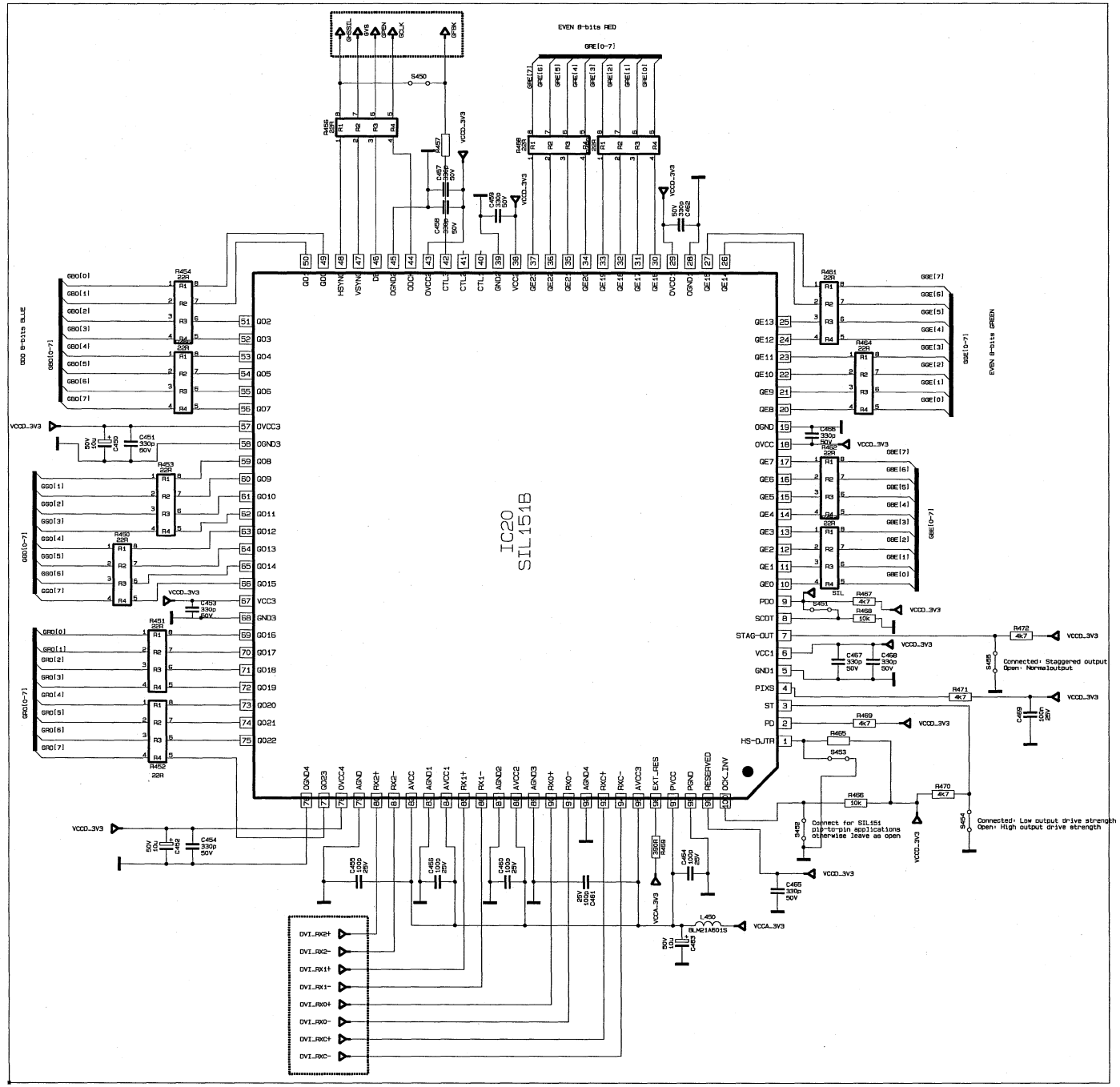

S101
S100
POWER_SWITCH
5R
PL100
F100
3. 15A
TH100
PL101
4kV
4n7
C100
R100
VAR-510V
4kV
4n7
C101
SW100

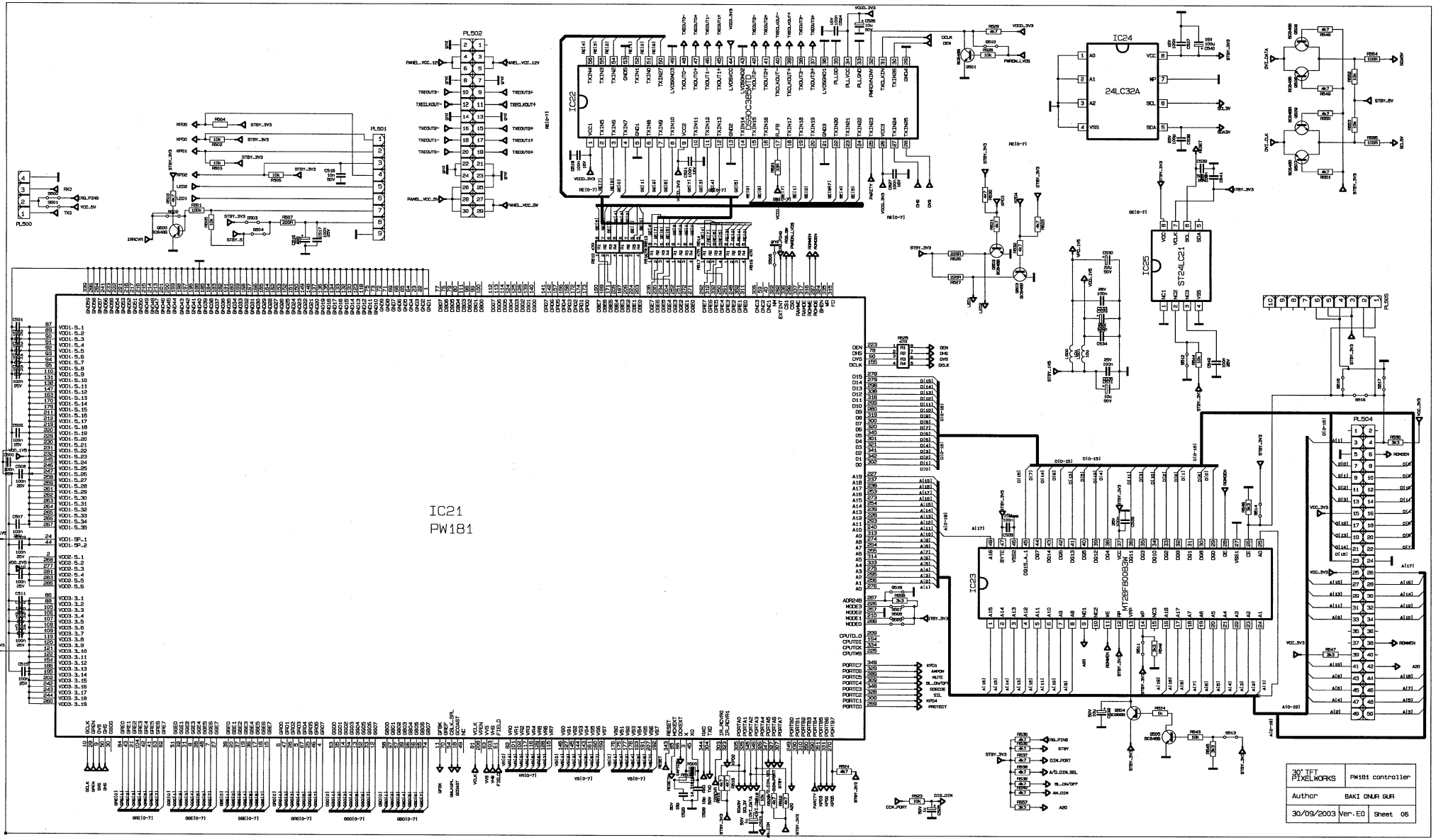

TU400
TUN-UR1316TMK3-HOR
TU101
TUN-UR1316TMK3-HOR
REMARK THAT: FOR PIP SPLITTER THERE SHOULD BE ADEQUATE PLACE IN LAY-OUT
TDA9885T
IC2
TDA9885T
IC7
TEA6415C
IC12
PCF8591
IC1
MSP3452G
IC6
TDA1308
K3563M
SF-63992
K3563M
FAV_IN
VxtoPIP
VxtoMAIN
SC2_V_OUT
SC1_V_OUT
TXT_CVBS
PIP_CVBS
TUN1_CVBS
SC1_V_IN
SC2_V_IN
TUN_CVBS
SCL_MAIN
SDA_MAIN
AUD_PIP
AM_SOUND
SCL_PIP
SDA_PIP
SCL5V
SDA5V
VCC_5V
VCC_8V
VCC_8V_A
ADC_CON
PIP_CVBS
SCART_AUDIO_OUT
SCART_AUDIO_IN
SUBW
MAINA_L
MAINA_R
PCM_CLK
PCM_LRCLK
PCM_DATA
AM_SOUND
I2C_SCL
I2C_SDA
ADR_SEL
STANDBY0
TO INTERNAL SPEAKERS

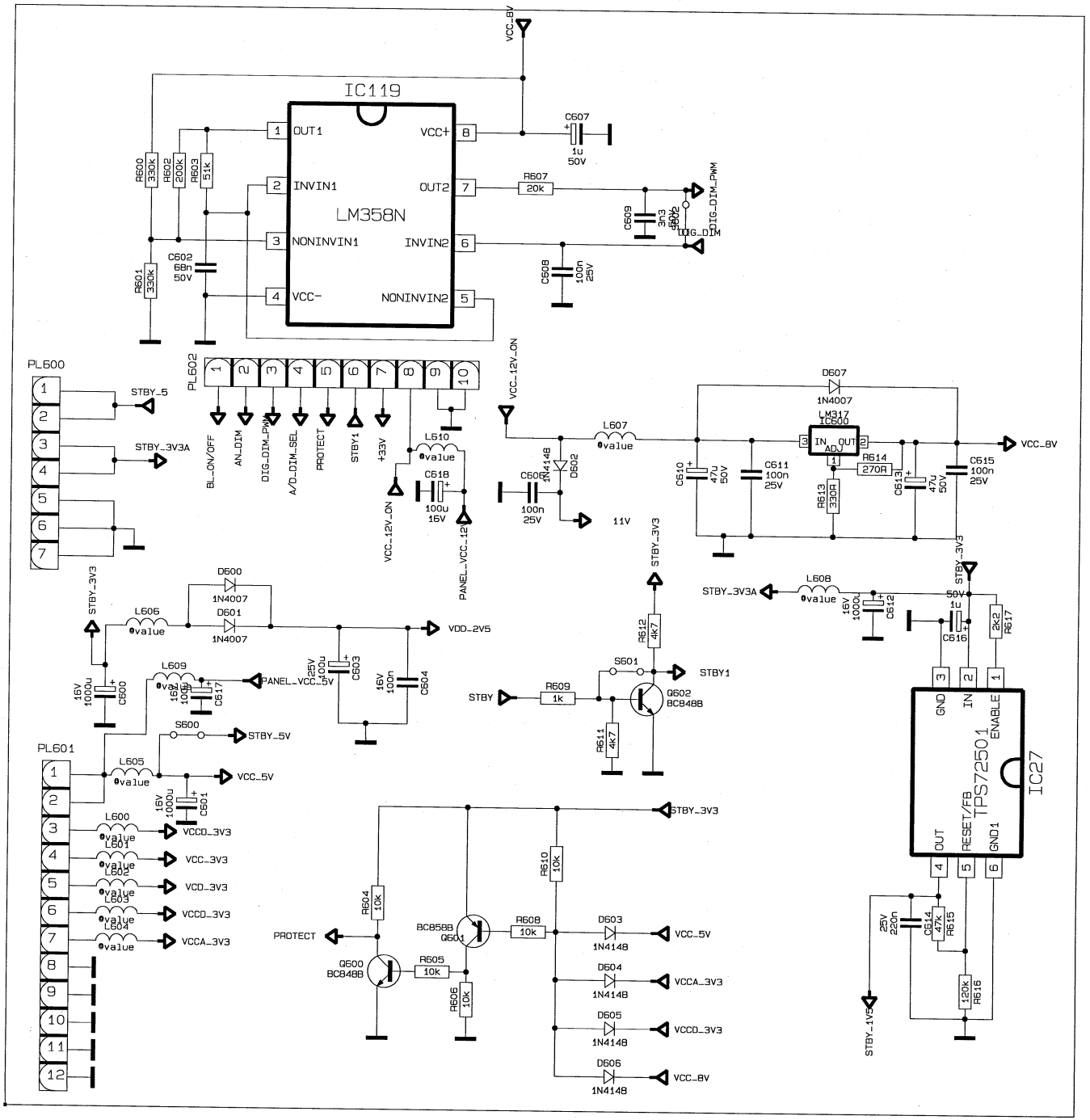

IC114
PI5V330_SOIC
IC113
ST24LC21
IC112
74LVC14A
IC111
ST24LC21
PL266
PL267
PL271
PL265
PL269
PL270
DDC_V
DDC_CLK_PC
DDC_DATA_PC
VCC_5V
PC_VS
DDCC_5V
DVI_RX0-
DVI_RX0+
DVI_RXC+
DVI_RXC-
DVI_VDDC
ANA_VS_DVI
DDC_DATA_DVI
DDC_CLK_DVI
DVI_RX1+
DVI_RX2+
DVI_RX2-
DVI_RX1-
LINE_L_OUT
LINE_R_OUT
PC_AUDIO_R_IN
PC_AUDIO_L_IN
VCCA_3V3
R68_SW
SC2_R
SC1_R
R_OUT
SC2_G
SC1_G
G_OUT
SC2_FB
SC1_FB
FB_OUT
SC2_B
SC1_B
B_OUT
SC2_AUDIO_R_OUT
SC2_AUDIO_L_OUT
SC2_AUDIO_L_IN
PIN8_SC2
SC2_V_OUT
SC2_V_IN
SC1_AUDIO_R_IN
SC1_AUDIO_L_IN
SC1_AUDIO_R_OUT
SC1_AUDIO_L_OUT
PIN8_SC1
SC1_V_OUT
SC1_V_IN
FAV_IN
CIN
SVHS_Y_IN
FAV_AUDIO_R_IN
FAV_AUDIO_L_IN
VCC_8V
MUTE
AMPON
SUBWF
MAINA_L
MAINA_R
CONNECTORS BETWEEN AUDIO AMP. BOARDS

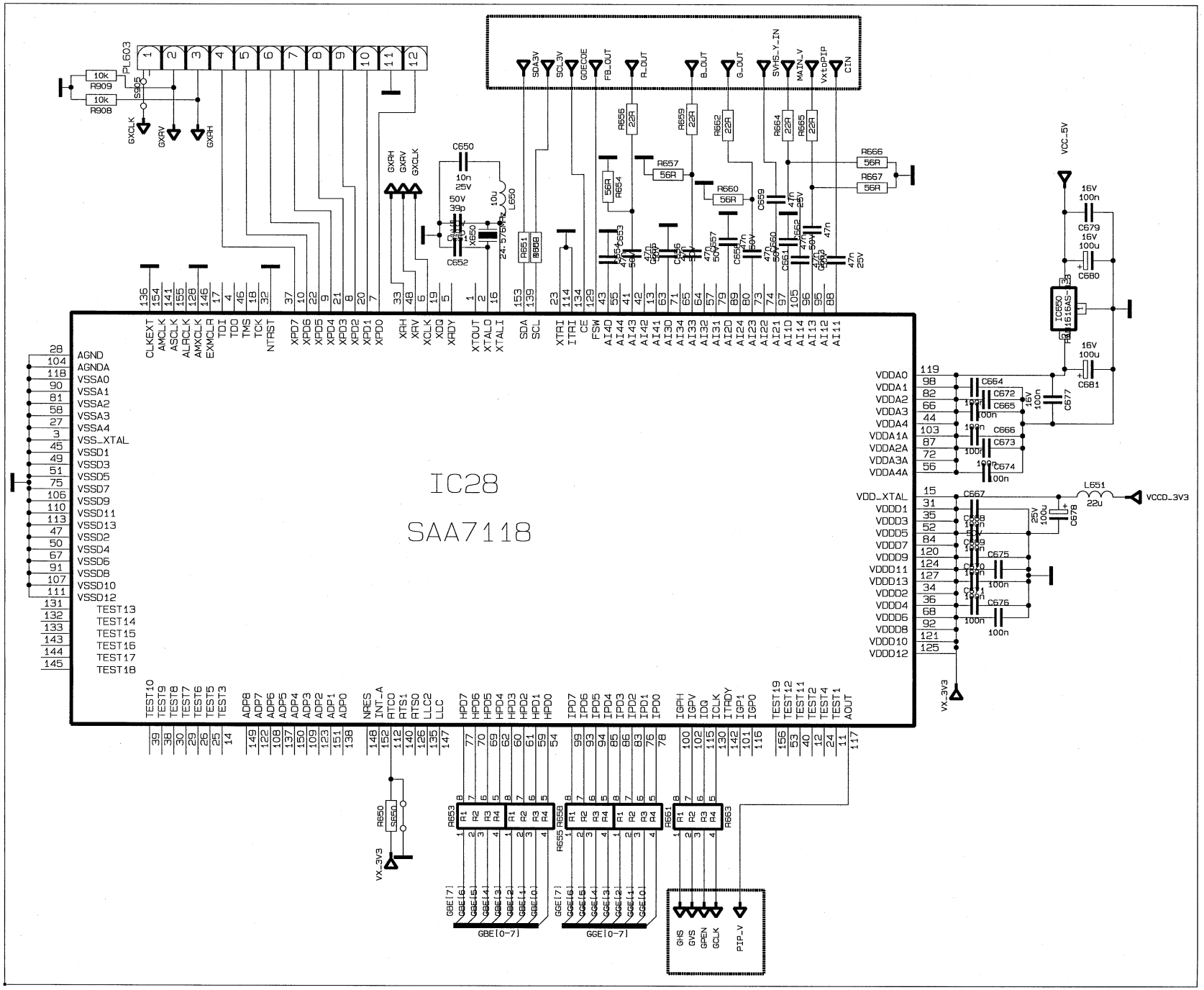

IC17
SAA7118
PL503
R509 10k
R508 10k
S505
X325 24.576MHz
C326 39p
C327 10n 25V
L325 10u
R327 68R
R330 56R
R332 22R
R333 56R
R335 22R
R336 56R
R338 22R
R339 22R
R341 22R
R342 22R
R343 22R
R344 56R
R347 56R
R345 56R
R346 56R
VXCLK
VXRV
VXRH
SDA3V
SCL3V
VY
MAIN_FB
MAIN_R
MAIN_B
MAIN_G
SVHS_Y_IN
PIP_V
VxtoMAIN
CIN
VCC_5V
IC325 1616A5
C353 16V 100n
C354 16V 100u
C355 16V 100u
C351
C339
C350
C340
C341
C342
L326 22u
VCCD_3V3
C343
C352 25V 100u
C344
C345
C346
C347
C348
C349
100n
TP325
TP326
TP328
R325 4x7
S325
R329
R331
R334
R337
R340
VUV[0-7]
VY[0-7]
VVCLK
VVHS
VVVS
VVPEN
FIELD
MAIN_V
CLKEXT
AMCLK
ASCLK
ALRCLK
AMXCLK
EXMCLR
TDI
TDO
TMS
TCK
NTRST
XPD7
XPD6
XPD5
XPD4
XPD3
XPD2
XPD1
XPD0
XRH
XRV
XCLK
XDQ
XRDY
XTOUT
XTALO
XTALI
SDA
SCL
XTRI
ITRI
CE
FSW
AI4D
AI44
AI43
AI42
AI41
AI3D
AI34
AI33
AI32
AI31
AI2D
AI24
AI23
AI22
AI21
AI1D
AI14
AI13
AI12
AI11
AGND
AGNDA
VSSA0
VSSA1
VSSA2
VSSA3
VSSA4
VSS_XTAL
VSSD1
VSSD3
VSSD5
VSSD7
VSSD9
VSSD11
VSSD13
VSSD2
VSSD4
VSSD6
VSSD8
VSSD10
VSSD12
TEST13
TEST14
TEST15
TEST16
TEST17
TEST18
VDDA0
VDDA1
VDDA2
VDDA3
VDDA4
VDDA1A
VDDA2A
VDDA3A
VDDA4A
VDD_XTAL
VDDD1
VDDD3
VDDD5
VDDD7
VDDD9
VDDD11
VDDD13
VDDD2
VDDD4
VDDD6
VDDD8
VDDD10
VDDD12
TEST10
TEST9
TEST8
TEST7
TEST6
TEST5
TEST3
ADP8
ADP7
ADP6
ADP5
ADP4
ADP3
ADP2
ADP1
ADP0
NRES
INT_A
RTCO
RTS1
RTS0
LLC2
LLC
HPD7
HPD6
HPD5
HPD4
HPD3
HPD2
HPD1
HPD0
IPD7
IPD6
IPD5
IPD4
IPD3
IPD2
IPD1
IPD0
IGPH
IGPV
IDQ
ICLK
ITHDY
IGP1
IGP0
TEST19
TEST12
TEST11
TEST2
TEST4
TEST1
AOUT

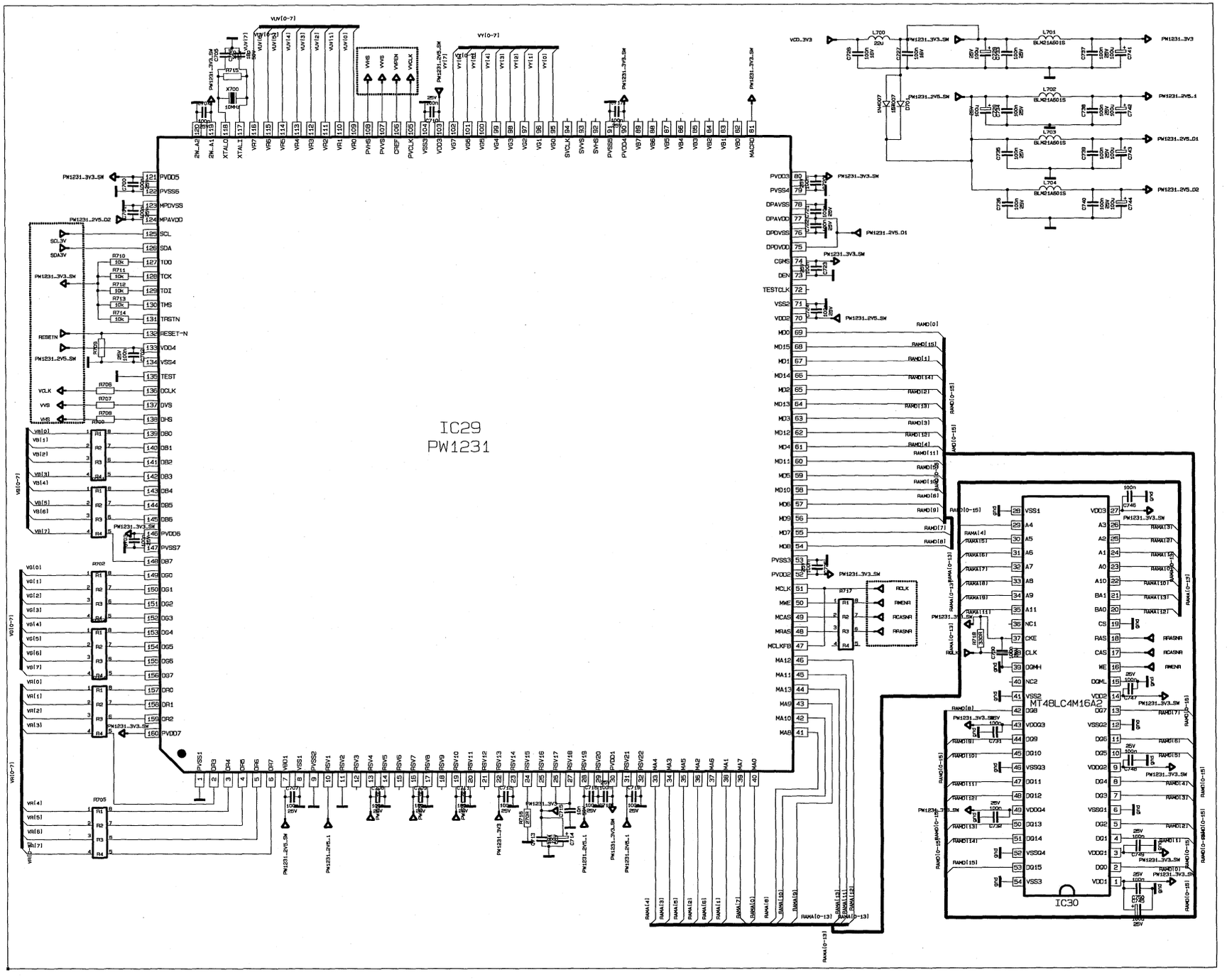

IC18
AD9883
IC19
74LVC257A
IC375
R1616AS-328
L375
10u
VCD_3V3
VCDGA1_3V3
SDA3V
SCL3V
PC_R
PC_G
PC_B
GHS
GHSSTL
HDSOG
GAFEDE
GDEDDE
GND16
VD9
REFBYPASS
SDA
SCL
A0
RAIN
GND15
VD8
VD7
GND14
SOGIN
GAIN
GND13
VD6
VD5
GND12
BAIN
VD4
GND11
GND17
VD10
GND18
VSOUT
SOGOUT
HSOUT
DATACK
GND19
VDD4
RED7
RED6
RED5
RED4
RED3
RED2
RED1
RED0
VDD5
VDD6
GND20
GND10
VD3
CLAMP
MIDSCV
GND9
PVD2
PVD1
FILT
GND8
VSYNC
HSYNC
COAST
GND7
VD2
VD1
GND6
GND5
VDD3
VDD2
GND4
GBLKSPL
VCC_5V
PC_VS
PC_HS
GCOAST
GVS
GFBK
GCLK
GRE[7]
GRE[6]
GRE[5]
GRE[4]
GRE[3]
GRE[2]
GRE[1]
GRE[0]
GRE[0-7]
GND1
GREEN7
GREEN6
GREEN5
GREEN4
GREEN3
GREEN2
GREEN1
GREEN0
GND2
VDD1
BLUE7
BLUE6
BLUE5
BLUE4
BLUE3
BLUE2
BLUE1
BLUE0
GND3
GGE[7]
GGE[6]
GGE[5]
GGE[4]
GGE[3]
GGE[2]
GGE[1]
GGE[0]
GGE[0-7]
GBE[7]
GBE[6]
GBE[5]
GBE[4]
GBE[3]
GBE[2]
GBE[1]
GBE[0]
GBE[0-7]
EVEN 8-bits BLUE

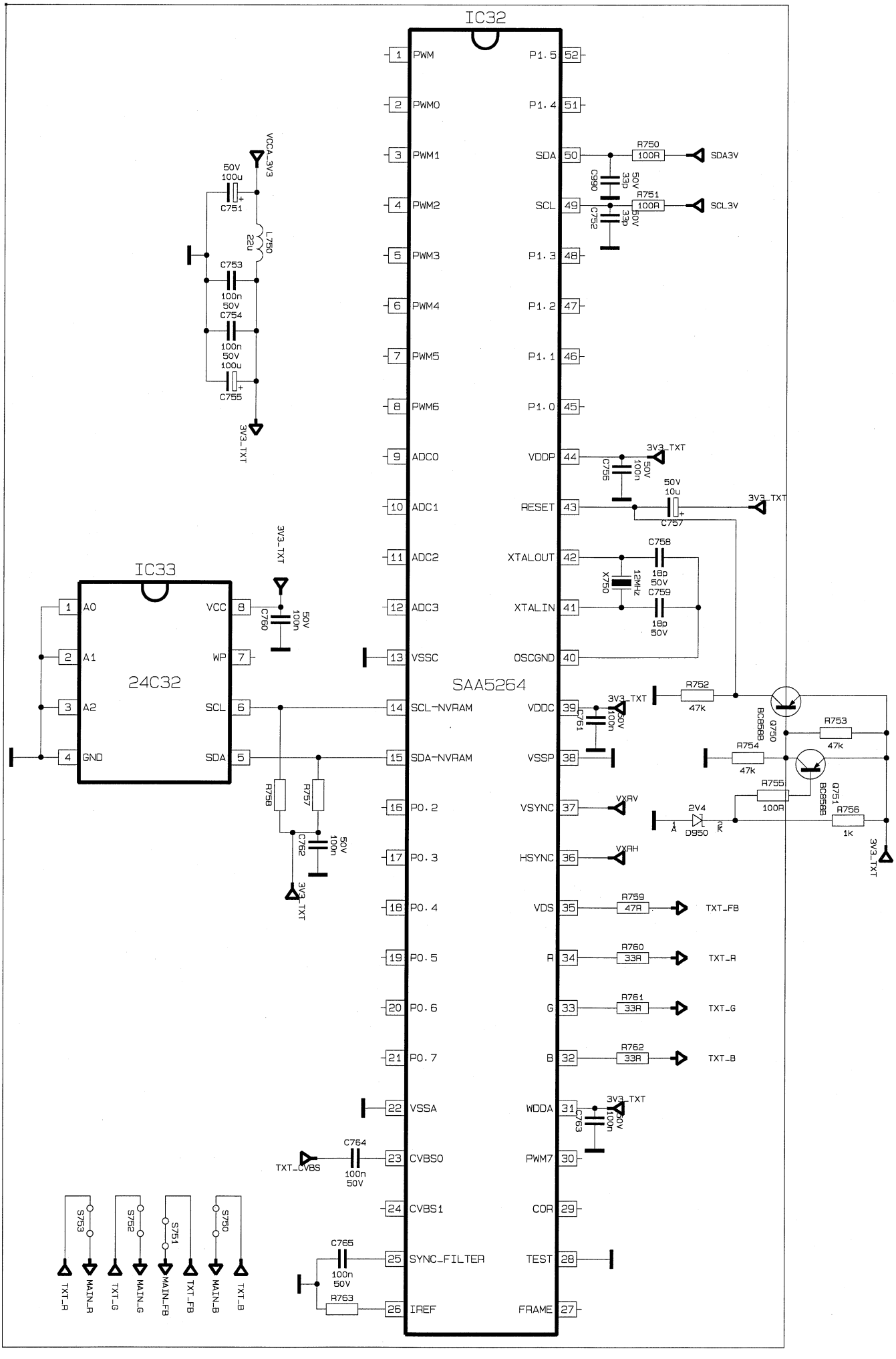

IC20
SIL151B
EVEN 8-bits RED
GRE[0~7]
ODD 8-bits BLUE
GBO[0~7]
ODD 8-bits GREEN
GGO[0~7]
ODD 8-bits RED
GRO[0~7]
EVEN 8-bits GREEN
GGE[0~7]
VCCD_3V3
VCCA_3V3
Connected: Staggered output
Open: Normaloutput
Connected: Low output drive strength
Open: High output drive strength
Connect for SIL151 pin-to-pin applications otherwise leave as open
DVI_RX2+
DVI_RX2-
DVI_RX1+
DVI_RX1-
DVI_RX0+
DVI_RX0-
DVI_RXC+
DVI_RXC-

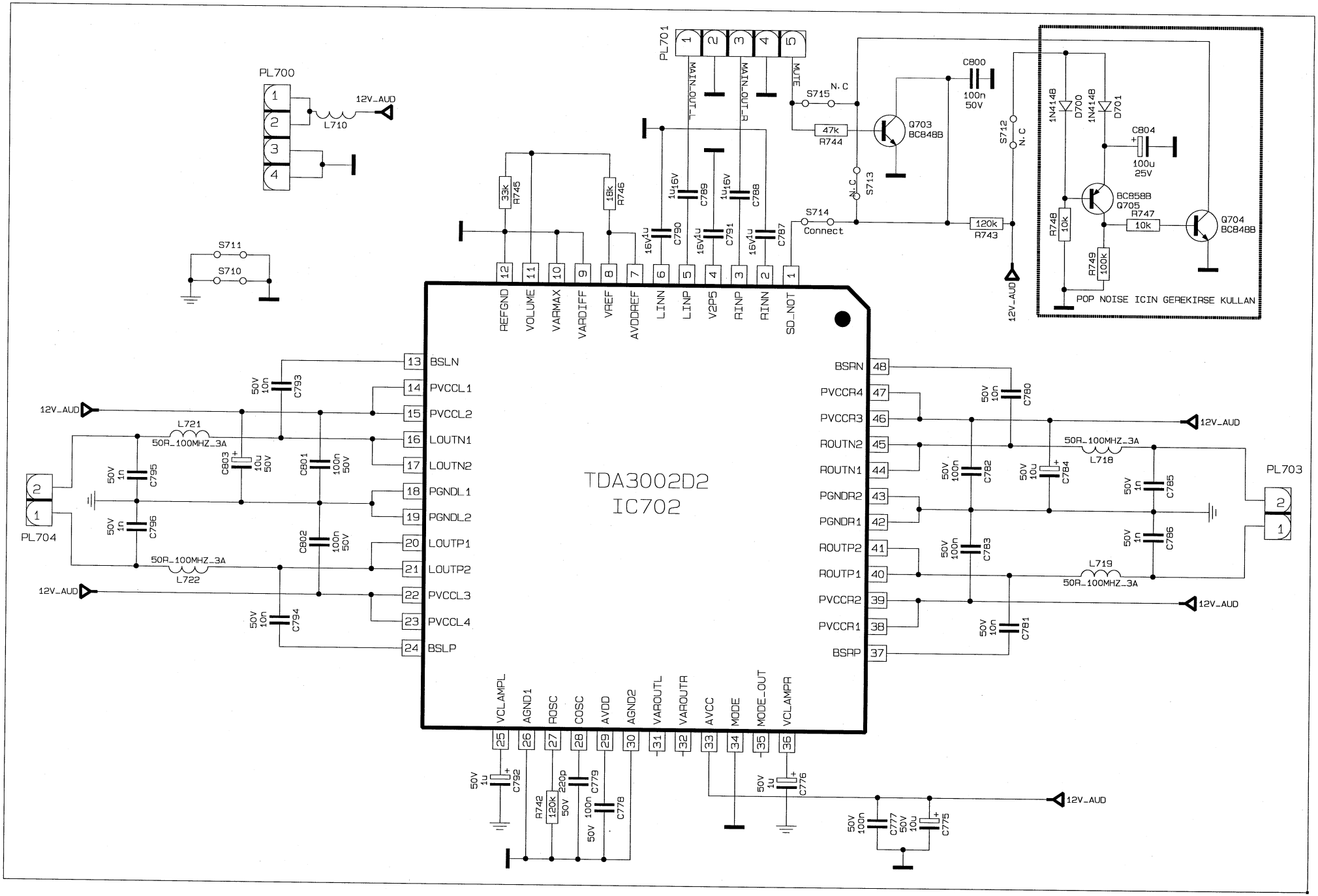

IC21
PW181
IC22
IC23
IC24
IC25
24LC32A
ST24LC21
PL500
PL501
PL502
PL503
PL504
PL505
30" TFT PIXELWORKS
PW181 controller
Author
BAKI ONUR GUR
30/09/2003
Ver.E0
Sheet 05

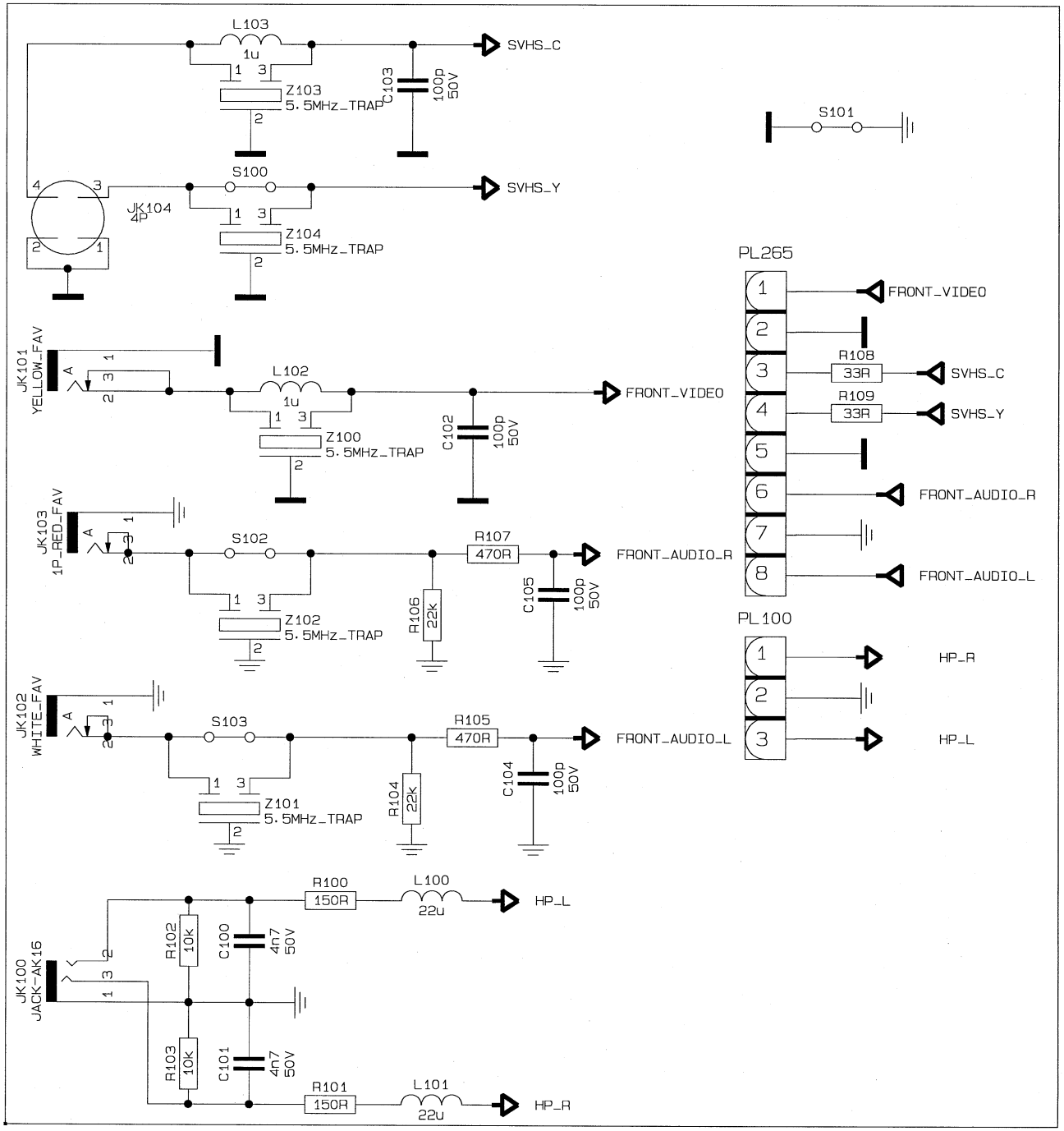

IC119
LM358N
OUT1
INVIN1
NONINVIN1
VCC-
VCC+
OUT2
INVIN2
NONINVIN2
VCC_8V
C607 1u 50V
R600 330k
R602 200k
R603 51k
R601 330k
C602 68n 50V
R607 20k
C609 3n3
C608 100n 25V
DIG_DIM_PWM
DIG_DIM
PL600
PL602
STBY_5
STBY_3V3A
BL_ON/OFF
AN_DIM
DIG_DIM_PWM
A/D_DIM_SEL
PROTECT
STBY1
+33V
VCC_12V_ON
PANEL_VCC_12
L610 0value
C618 100u 16V
VCC_12V_ON
L607 0value
D602 1N4148
C606 100n 25V
11V
D607 1N4007
LM317 IC600
IN ADJ OUT
R614 270R
R613 330R
C610 47u 50V
C611 100n 25V
C613 47u 50V
C615 100n 25V
VCC_8V
D600 1N4007
D601 1N4007
L605 0value
VDD_2V5
C600 16V 1000u
L609 0value
C617 100n
PANEL_VCC_5V
C603 25V 1000u
C604 16V 100n
S600
STBY_5V
PL601
L605 0value
VCC_5V
C601 16V 1000u
L600 0value
VCCD_3V3
L601 0value
VCC_3V3
L602 0value
VCD_3V3
L603 0value
VCCD_3V3
L604 0value
VCCA_3V3
STBY_3V3
R612 4K7
S601
STBY1
STBY
R609 1k
Q602 BC848B
R611 4K7
L608 0value
C612 16V 1000u
C616 50V 1u
R617 2k2
IC27
TPS72501
GND
IN
ENABLE
OUT
RESET/FB
GND1
C614 25V 220n
R615 47k
R616 120k
STBY_1V5
R610 10k
R604 10k
PROTECT
Q600 BC848B
R605 10k
R606 10k
Q601 BC858B
R608 10k
D603 1N4148
D604 1N4148
D605 1N4148
D606 1N4148
VCC_5V
VCCA_3V3
VCCD_3V3
VCC_8V

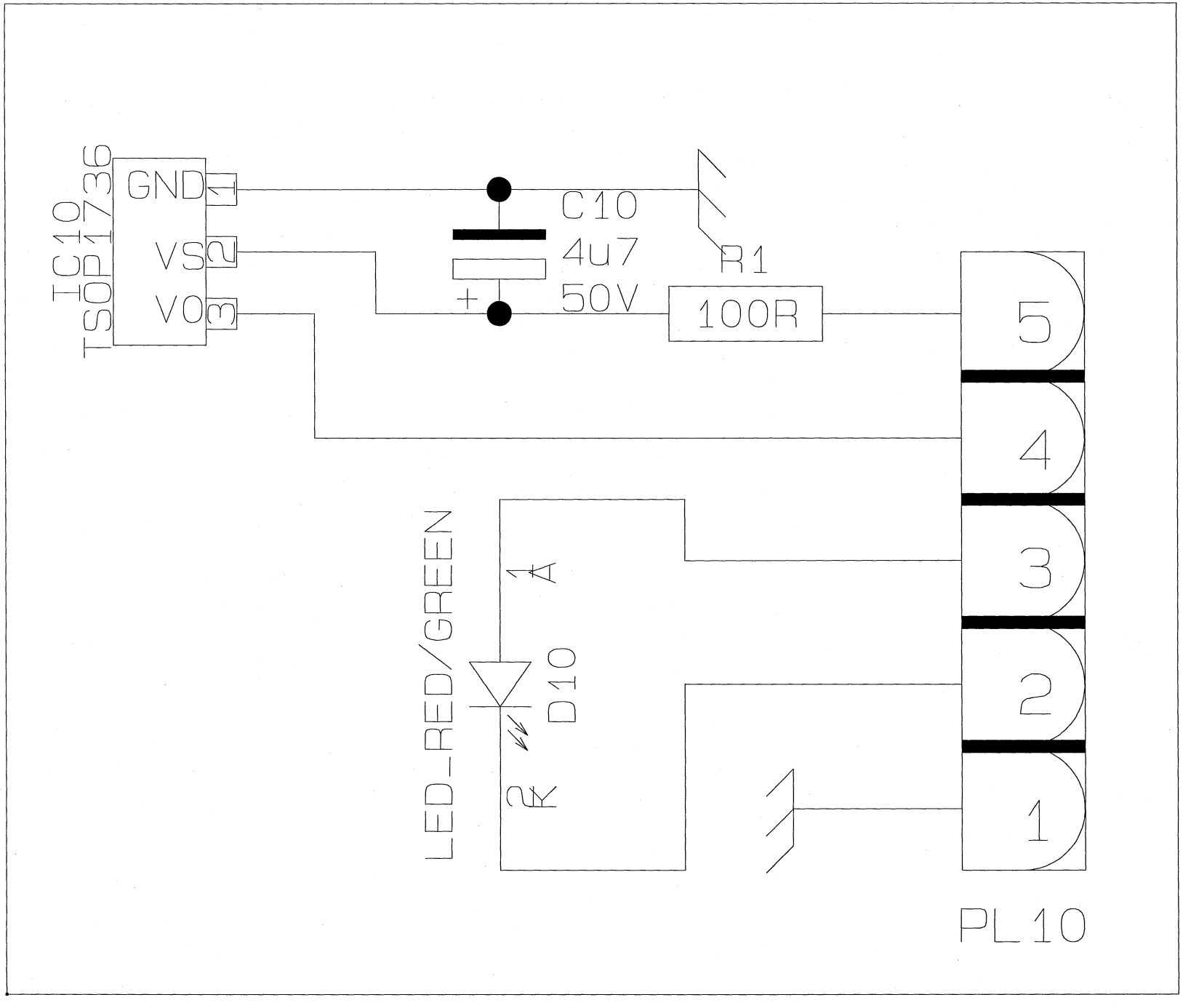

IC28
SAA7118
PL603
R909
R908
S905
GXCLK
GXRV
GXRH
C650
L650
X650
24.576MHz
C652
R651
R658
R656
R654
R657
R660
R659
R662
R664
R665
R666
R667
C659
C661
C662
C663
C654
C655
C657
C660
SDA3V
SCL3V
GDECODE
FB_OUT
R_OUT
B_OUT
G_OUT
SVHS_Y_IN
MAIN_V
VxtoPIP
CIN
VCC_5V
C679
C680
IC650
C681
C677
C664
C672
C665
C666
C673
C674
L651
22u
VCCD_3V3
C667
C678
C675
C676
VX_3V3
R650
S650
R653
R658
R655
R661
R663
GBE[0-7]
GGE[0-7]
GHS
GVS
GPEN
GCLK
PIP_V
AGND
AGNDA
VSSA0
VSSA1
VSSA2
VSSA3
VSSA4
VSS_XTAL
VSSD1
VSSD3
VSSD5
VSSD7
VSSD9
VSSD11
VSSD13
VSSD2
VSSD4
VSSD6
VSSD8
VSSD10
VSSD12
TEST13
TEST14
TEST15
TEST16
TEST17
TEST18
VDDA0
VDDA1
VDDA2
VDDA3
VDDA4
VDDA1A
VDDA2A
VDDA3A
VDDA4A
VDD_XTAL
VDDD1
VDDD3
VDDD5
VDDD7
VDDD9
VDDD11
VDDD13
VDDD2
VDDD4
VDDD6
VDDD8
VDDD10
VDDD12
CLKEXT
AMCLK
ASCLK
ALRCLK
AMXCLK
EXMCLR
TDI
TDO
TMS
TCK
NTRST
XPD7
XPD6
XPD5
XPD4
XPD3
XPD2
XPD1
XPD0
XRH
XRV
XCLK
XDQ
XRDY
XTOUT
XTALO
XTALI
SDA
SCL
XTRI
ITRI
CE
FSW
AI4D
AI44
AI43
AI42
AI41
AI3D
AI34
AI33
AI32
AI31
AI2D
AI24
AI23
AI22
AI21
AI1D
AI14
AI13
AI12
AI11
TEST10
TEST9
TEST8
TEST7
TEST6
TEST5
TEST3
ADP8
ADP7
ADP6
ADP5
ADP4
ADP3
ADP2
ADP1
ADP0
NRES
INT_A
RTCO
RTS1
RTS0
LLC2
LLC
HPD7
HPD6
HPD5
HPD4
HPD3
HPD2
HPD1
HPD0
IPD7
IPD6
IPD5
IPD4
IPD3
IPD2
IPD1
IPD0
IGPH
IGPV
IDQ
ICLK
ITRDY
IGP1
IGP0
TEST19
TEST12
TEST11
TEST2
TEST4
TEST1
AOUT

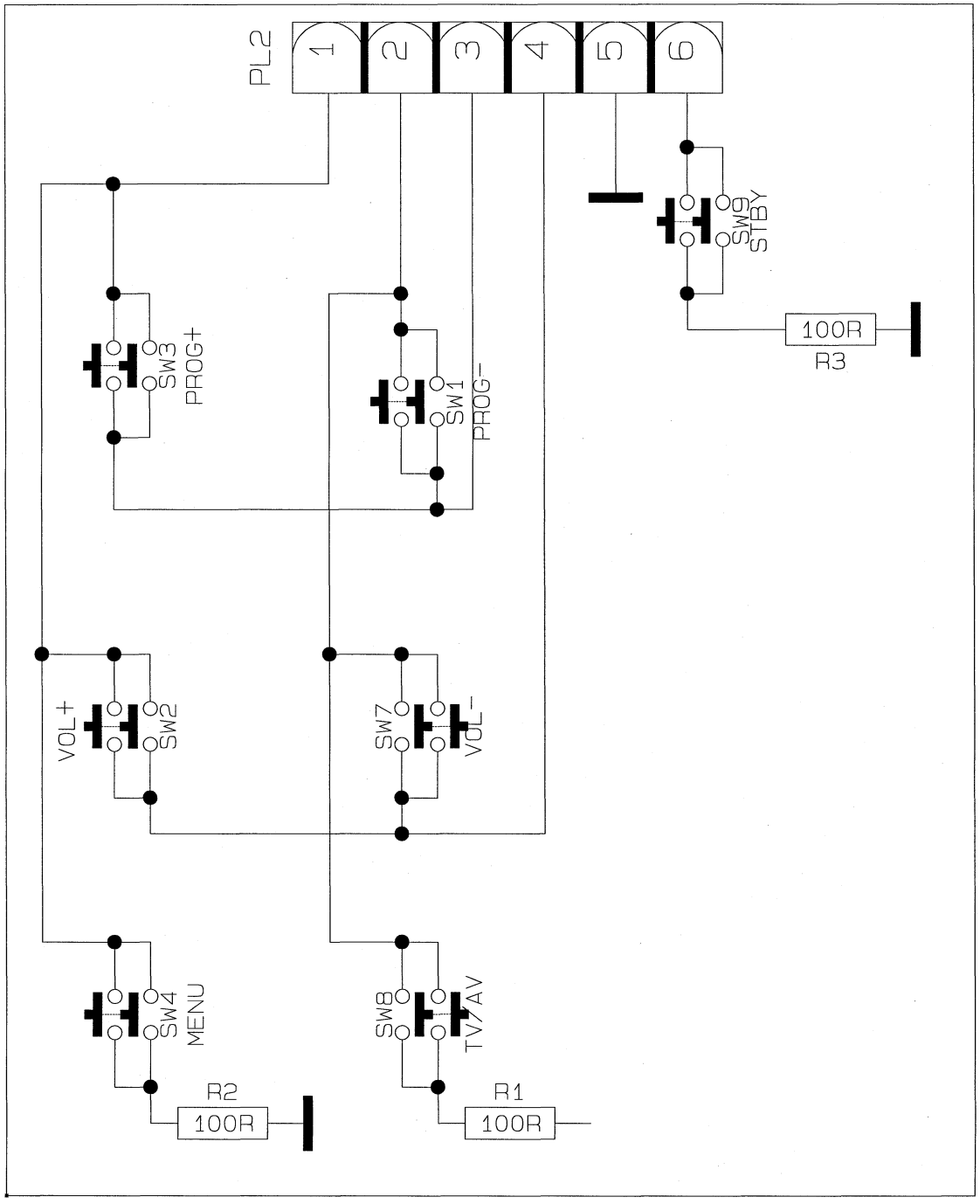

IC29
PW1231
IC30
MT48LC4M16A2


IC32
SAA5264
IC33
24C32
1 PWM
2 PWM0
3 PWM1
4 PWM2
5 PWM3
6 PWM4
7 PWM5
8 PWM6
9 ADC0
10 ADC1
11 ADC2
12 ADC3
13 VSSC
14 SCL-NVRAM
15 SDA-NVRAM
16 P0.2
17 P0.3
18 P0.4
19 P0.5
20 P0.6
21 P0.7
22 VSSA
23 CVBS0
24 CVBS1
25 SYNC_FILTER
26 IREF
52 P1.5
51 P1.4
50 SDA
49 SCL
48 P1.3
47 P1.2
46 P1.1
45 P1.0
44 VDDP
43 RESET
42 XTALOUT
41 XTALIN
40 OSCGND
39 VDDC
38 VSSP
37 VSYNC
36 HSYNC
35 VDS
34 R
33 G
32 B
31 WDDA
30 PWM7
29 COR
28 TEST
27 FRAME
VCCA_3V3
C751 50V 100u
L750 22u
C753 100n 50V
C754 100n 50V
C755 100u
3V3_TXT
C760 50V 100n
C762 50V 100n
R758
R757
C761 50V 100n
C763 50V 100n
C764 100n 50V
C765 100n 50V
R763
C756 50V 100n
C757 50V 10u
C758 18p 50V
C759 18p 50V
X750 12MHz
R750 100R
R751 100R
C750 33p 50V
C752 33p 50V
SDA3V
SCL3V
R752 47k
R753 47k
R754 47k
R755 100R
R756 1k
R759 47R
R760 33R
R761 33R
R762 33R
Q750 BC858B
Q751 BC858B
D950 2V4
VXRV
VXRH
TXT_FB
TXT_R
TXT_G
TXT_B
TXT_CVBS
S753
S752
S751
S750
TXT_R
MAIN_R
TXT_G
MAIN_G
MAIN_FB
TXT_FB
MAIN_B
TXT_B


TDA3002D2
IC702
PL700
PL701
PL703
PL704
L710
L718
L719
L721
L722
S710
S711
S712
S713
S714
S715
N.C
Connect
Q703 BC848B
Q704 BC848B
Q705 BC858B
D700 1N4148
D701 1N4148
R743 120k
R744 47k
R745 33k
R746 18k
R747 10k
R748 10k
R749 100k
C787
C788
C789
C790
C791
C800 100n 50V
C804 100u 25V
POP NOISE ICIN GEREKIRSE KULLAN
12V_AUD
MAIN_OUT_L
MAIN_OUT_R
MUTE
REFGND
VOLUME
VARMAX
VARDIFF
VREF
AVDDREF
LINN
LINP
V2P5
RINP
RINN
SD_NOT
BSLN
PVCCL1
PVCCL2
LOUTN1
LOUTN2
PGNDL1
PGNDL2
LOUTP1
LOUTP2
PVCCL3
PVCCL4
BSLP
BSRN
PVCCR4
PVCCR3
ROUTN2
ROUTN1
PGNDR2
PGNDR1
ROUTP2
ROUTP1
PVCCR2
PVCCR1
BSRP
VCLAMPL
AGND1
ROSC
COSC
AVDD
AGND2
VAROUTL
VAROUTR
AVCC
MODE
MODE_OUT
VCLAMPR
50R_100MHZ_3A


L103
1u
Z103
5.5MHz_TRAP
C103
100p
50V
SVHS_C
S100
SVHS_Y
JK104
4P
Z104
5.5MHz_TRAP
S101
PL265
JK101
YELLOW_FAV
L102
1u
Z100
5.5MHz_TRAP
C102
100p
50V
FRONT_VIDEO
R108
33R
SVHS_C
R109
33R
SVHS_Y
JK103
1P_RED_FAV
S102
R107
470R
FRONT_AUDIO_R
Z102
5.5MHz_TRAP
R106
22k
C105
100p
50V
FRONT_AUDIO_L
PL100
JK102
WHITE_FAV
S103
R105
470R
Z101
5.5MHz_TRAP
R104
22k
C104
100p
50V
HP_R
HP_L
JK100
JACK-AK16
R100
150R
L100
22u
R102
10k
C100
4n7
50V
R103
10k
C101
4n7
50V
R101
150R
L101
22u


IC10
TSOP1736
GND
VS
VO
1
2
3
C10
4u7
50V
+
R1
100R
5
4
3
2
1
LED_RED/GREEN
1
A
D10
2
K
PL10


PL2
1
2
3
4
5
6
SW9
STBY
100R
R3
SW3
PROG+
SW1
PROG-
VOL+
SW2
SW7
VOL-
SW4
MENU
SW8
TV/AV
R2
100R
R1
100R